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<u>L19</u>	l7 and l9	43	<u>L19</u>
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<u>L6</u>	(one or single) near5 (clock or cycle) near10 (issu\$5 or send\$3 or sent or transfer\$5)	19175	<u>L6</u>
<u>L5</u>	interlock\$5 near15 scalar near15 vector	9	<u>L5</u>
<i>DB=USPT; PLUR=YES; OP=OR</i>			
<u>L4</u>	L3 and (cycle or clock)	1	<u>L4</u>

L3 5574927.pn.

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IEEE JNL	IEE Journal or Magazine
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Binkley, D.M.; Young, J.W.;  
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Volume 45, Issue 3, Part 1, June 1998 Page(s):782 - 786  
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[Circuits and Systems II: Analog and Digital Signal Processing, IEEE Transactions on](#) [see also [Circuits and Systems I: Analog and Digital Signal Processing, IEEE Transactions on](#)]  
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Peleg, M.; Sason, I.; Shamai, S.; Elia, A.;  
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Merhav, N.;  
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Xiaodong Wang; Rong Chen;  
[Signal Processing, IEEE Transactions on](#) [see also [Acoustics, Speech, and Signal Processing, IEEE Transactions on](#)]  
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- ☐ **13. Blind turbo equalization in Gaussian and impulsive noise**  
Xiaodong Wang; Rong Chen;  
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**14. The throughput of hybrid-ARQ protocols for the Gaussian collision channel**

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**17. High-performance 1-Gb-NAND flash memory with 0.12- $\mu\text{m}$  technology**

June Lee; Heung-Soo Im; Dae-Seok Byeon; Kyeong-Han Lee; Dong-Hyuk Chae; Kyong-Hwa Lee;  
Sung-Soo Lee; Young-Ho Lim; Jae-Duk Lee; Jung-Dal Choi; Young-Il Seo; Jong-Sik Lee; Kang-De  
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**19. A Gaussian noise generator for hardware-based simulations**

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**20. Estimating the Manufacturing Yield of Compiler-Based Embedded SRAMs**

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Dong, B.; Xiaodong Wang;

[Communications, IEEE Transactions on](#)

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#### 23. Interactive built-in self-test compression for testing a system-on-a-chip

Kay, D.; Mourad, S.;

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#### 24. Low power BIST based on scan partitioning

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IEEE CNF	IEEE Conference Proceeding
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


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Shaou-Gang Miaou; Shih-Tse Chen; Fu-Sheng Ke;  
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Shaou-Gang Miaou; Shih-Tse Chen; Chih-Hong Hsiao;  
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[VLSI Design, 2004. Proceedings. 17th International Conference on](#)  
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[Acoustics, Speech, and Signal Processing, 2003. Proceedings. \(ICASSP '03\). 2003 IEEE International Conference on](#)  
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